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THERMAL INTERFACIAL RESISTANCE REDUCTION BETWEEN METAL AND DIELECTRIC MATERIALS BY INSERTING INTERMEDIATE METAL LAYER

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ABSTRACT

In this work, we have observed 60% reduction in total interfacial resistance by adding an intermediate metal layer nickel between gold and aluminum oxide. Two temperature model is applied to explain the change of interfacial resistance, including both lattice mismatch with diffuse mismatch model and electron-phonon coupling effect. Simulation result agrees reasonably well with experimental data. Even though interfacial resistance due to electron-phonon coupling effect for Au-aluminum oxide is much larger than that of Ni-aluminum oxide interface, lattice mismatch is still the dominant factor for interfacial resistance.

R_{pp} Phonon mismatch thermal resistance
 T_e Electron temperature
 T_p Phonon temperature
 $v_{1\omega}$ Voltage signal of ω frequency
 $v_{3\omega}$ Voltage signal of 3ω frequency
 α Transmission coefficient
 ΔT Temperature oscillation amplitude
 ΔT_R Temperature oscillation amplitude for reference sample
 ΔT_{F+R} Temperature oscillation amplitude for thin film sample
 δ Kronecker delta function
 \hbar Plank's constant
 ω Frequency of current in 3ω measurement

NOMENCLATURE

b Half width of metal line
 C_{rt} Coefficient between resistance and temperature
 D Thermal diffusivity
 DOS Phonon density of state
 f Bose-Einstein distribution function
 G_{ep} Electron-phonon coupling factor
 h Thermal interfacial conductance
 k Thermal conductivity
 k_e Electron thermal conductivity
 k_p Phonon thermal conductivity
 l Length of metal line
 p Power assumption of metal line
 q^{-1} penetration depth
 R_{diff} Thermal resistance difference
 R_{ei} Inelastic electron scattering thermal resistance
 R_{ep} Electron-phonon coupling thermal resistance

INTRODUCTION

It has become crucial to measure and evaluate thermal interfacial resistance between metal and dielectric materials, especially when thin films are widely applied in integrated circuits. As the thickness of films shrinks to micro/nano-scale, the interfacial resistance between metal and dielectric materials ($\sim 10^{-8} \text{m}^2\text{K/W}$ [1–5]) is becoming comparable to the thermal resistance of thin films ($\sim 10 \text{nm}$) in various engineering applications [6–8]. In these scenarios, interfacial thermal resistance has become a challenge for thermal management of nano-scale electronic devices. Gold has been widely used for electrical conductance due to its high conductance and chemical stability. However, thermal interfacial resistance between gold and dielectric materials is still less than optimal.

The theoretical estimation of interfacial resistance between metal and dielectric materials is of great interest by various

groups. There are many approaches to estimate interfacial resistance, such as acoustic mismatch model (AMM) [9, 10], and diffuse mismatch model (DMM) [10]. These two models can reach reasonable agreement with experiments at low temperatures below 40K depending on different interface conditions. Certain modifications [8] are proposed for high temperatures, by introducing and relying on parameters fitted with experimental data, though the use of such fitting parameters may make the agreement coincidental. Atomic level simulations such as molecular dynamics [11–15], Green’s function method [16, 17] have also been used for interfacial resistance, with inputs of only lattice structures and interatomic potentials. However, due to their high computational cost [11], they are more suitable for low-dimensional system. The methods mentioned above can only account for thermal transfer through phonons, ignoring the contribution of other carriers. For metals, semimetals, or heavily doped semiconductors where electron is main carrier of heat transfer, its contribution to heat transfer cannot be neglected. Due to differences in main heat carriers, electrons and phonons at different sides of the interface between metal and dielectric materials may have a large difference in kinetic energy, when electron-phonon coupling effect could be significant [18, 19]. It is more reasonable to combine both electron-phonon coupling effect with phonon mismatch model than to apply either of them alone. It is widely accepted that the metal-dielectric interfacial resistance includes not only phonon-phonon scattering, but also electron-phonon coupling effect at the interface as well as electron inelastic scattering across the interface. A simple approach to implement electron-phonon coupling effect is two-temperature model [18, 20–24], assuming two different temperatures for phonons and electrons separately across the interface.

In this work, we have fabricated three layered structures on silicon substrate, consisting of Ni, Au, and aluminum oxide layers. Thermal interfacial resistance characterization is done by 3ω method. We observed around 60% reduction of total interfacial resistance by adding nickel layer between gold and aluminum oxide. Two temperature model is applied to explain the change of interfacial resistance, including both lattice mismatch with diffuse mismatch model and electron-phonon coupling effect. Simulation results agree well with experimental results. Even though interfacial resistance due to electron-phonon coupling effect for Au-aluminum oxide is much larger than that for Ni-aluminum oxide, lattice mismatch is still the dominant factor for interfacial resistance between nickel, gold and aluminum oxide.

SAMPLE FABRICATION

Two sandwich structures and a reference sample are fabricated to determine interfacial resistance between gold and aluminum oxide layer, with and without nickel intermediate layer, shown in Fig.1, where white color stands for silicon substrate, yellow for gold layer, dark gray for aluminum oxide, and light

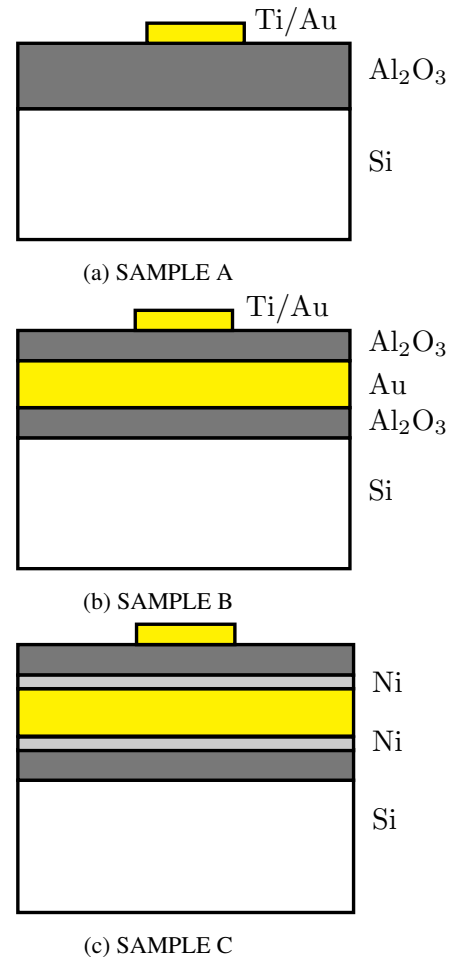


FIGURE 1: SANDWICH STRUCTURES FABRICATED WITH MULTIPLE LAYERS

gray for nickel. Silicon substrate is first cleaned with RCA method [25] (the Radio Corporation of America), and HF dip method to remove any oxidation layer and contamination. It is crucial to remove any oxidation layer due to low thermal conductivity of silicon oxide. The first structure, sample A, as the reference sample, has 60nm aluminum oxide layer on silicon substrate. The second one, sample B, consists of 30nm aluminum oxide layer, 50nm gold layer, and another 30nm aluminum oxide layer on top. And sample C inserts 20nm nickel layers between gold and aluminum oxide layers. Aluminum oxide layers are deposited with atomic layer deposition to ensure a consistent thickness, and metal layers are made with evaporation. Above the surfaces of all multi-layer structures, a metal line of $30\mu\text{m}$ wide and 3mm long is deposited with 20nm Ti and 100nm Au using photolithography for 3ω thermal characterization.

THERMAL INTERFACIAL RESISTANCE CHARACTERIZATION

The differential 3ω method [26] is used to characterize interfacial resistance in our work. It is developed to measure thin film thermal conductivity and interfacial resistance based on 3ω method [27, 28] that requires a thin metal line on the surface of the sample. Because of the micro-scale of the metal line, radiation loss even at high temperature is insignificant. During the measurement, an AC current of frequency ω is applied to the metal line, heating up the surface of the sample, resulting in a temperature oscillation amplitude of ΔT , and a voltage of 3ω frequency across the metal line. Three lock-in amplifiers are used to measure $v_{1\omega}$, $v_{3\omega}$ and power consumption p of the metal line. Detailed mathematic deductions can be found in previous literatures [27, 28]. For bulk materials, ΔT can be expressed as Eq.(1)

$$\Delta T = \frac{2v_{3\omega}}{v_{1\omega}C_{rt}} = \frac{p}{\pi k} \int_0^\infty \frac{\sin^2(\lambda b)}{(\lambda b)^2(\lambda^2 + 2i\omega/D)^{1/2}} d\lambda \quad (1)$$

where k , D is the thermal conductivity and thermal diffusivity of the bulk sample, b is half width of metal line, and C_{rt} is temperature coefficient for metal line. For measurement of film conductivity or interfacial resistance, 2 structures are measured separately, one reference sample without thin films, another with thin films, leading to different ΔT of metal lines even with the same power consumption, according to Eq.(2). By subtracting temperature oscillation amplitude of reference sample from that of thin film structure, temperature drop across thin film can be obtained. If we define thermal impedance as temperature change over power consumption $\Delta T/p$, the thermal impedance difference between two samples can be calculated with Eq.(3).

$$\Delta T_{R+F} = \Delta T_R + \frac{p}{2bl} R_{diff} \quad (2)$$

$$R_{diff} = 2bl \left(\left(\frac{\Delta T}{p} \right)_{R+F} - \left(\frac{\Delta T}{p} \right)_R \right) \quad (3)$$

Results from 3ω measurement are shown in Fig.2. Comparing results of sample B and sample C with reference sample A, we can obtain thermal impedance difference ΔR_1 , and ΔR_2 as in Eq.(7), (8), where interfacial resistance between Ni and Au is neglected due to its small value. We observed around 60% reduction of total interfacial resistance (from $9.52 \times 10^{-8} \text{m}^2\text{K/W}$ to $2.68 \times 10^{-8} \text{m}^2\text{K/W}$) by adding nickel layer between gold and aluminum oxide. With differential 3ω method, thermal interfacial resistance between gold and aluminum oxide is $4.84 \pm 0.13 \times 10^{-8} \text{m}^2\text{K/W}$, and that between nickel and aluminum oxide is $1.40 \pm 0.14 \times 10^{-8} \text{m}^2\text{K/W}$.

$$R_A = 2R_{AlO} + R_{Si-AlO} \quad (4)$$

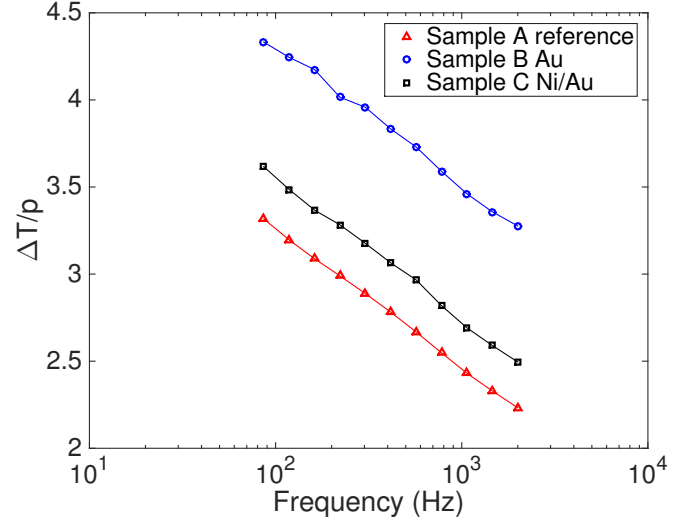


FIGURE 2: 3ω MEASUREMENT FOR THREE SANDWICH STRUCTURES

$$R_B = 2R_{AlO} + R_{Si-AlO} + 2R_{Au-AlO} \quad (5)$$

$$R_C = 2R_{AlO} + R_{Si-AlO} + 2R_{Ni-AlO} + R_{Au-Ni} \quad (6)$$

$$\Delta R_1 = R_B - R_C = 2R_{Au-AlO} \quad (7)$$

$$\Delta R_2 = 2R_{Ni-AlO} + R_{Au-Ni} \approx 2R_{Ni-AlO} \quad (8)$$

TWO TEMPERATURE MODEL ON INTERFACES

Two-temperature model assumes two different temperatures for phonons and electrons separately across the interface. The overall interfacial resistance consists of a phonon-phonon component R_{pp} , an electron-phonon component R_{ep} , as well as electrical inelastic scattering one, as shown in Fig.3 [29]. The third one R_{ei} is usually neglected due to its much higher resistance, since electron density in dielectric materials is low.

The first effect is the same with that for interfaces between dielectric-dielectric materials, where phonons in one side of interface transfer into the other side. Detailed diffuse mismatch model is applied to model phonon-phonon interface resistance in this work [10], which assumes phonons lose their correlations and randomize directions across the interface. Detailed phonon dispersions are calculated by lattice dynamics for nickel, gold and aluminum oxide in Fig.4. It should be noted that nickel is polycrystalline and aluminum oxide is amorphous in experiments due to the limitations of fabrication, while in lattice dynamics we assume crystalline structure for simplifications. In Fig.4, there are numerous branches of phonons for aluminum oxide. However, phonon frequencies in nickel and gold only overlap mostly with the acoustic phonons in aluminum oxide.

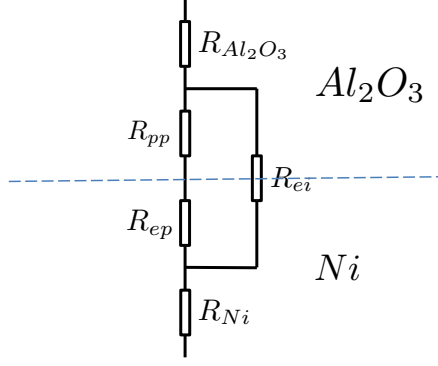


FIGURE 3: Thermal Resistance Network Between Nickel and Aluminum Oxide

So only these acoustic branches of aluminum oxide phonons are considered in the DMM calculation, since transmission between metal phonons and the other phonons of aluminum oxide is negligible. Detailed balance is enforced as $\alpha_{A \rightarrow B} = \alpha_{B \rightarrow A}$ to obtain transmission coefficient α . Equation(9), (10) are used for calculating interfacial resistance from material A to material B,

$$h_{A \rightarrow B} = \frac{1}{4} \sum_j \int \hbar \omega \text{DOS}_{A,j} v_{A,j} \alpha_{A \rightarrow B} \frac{\partial f}{\partial T} d\omega \quad (9)$$

$$\alpha_{A \rightarrow B}(\omega') = \frac{\sum_j \text{DOS}_{B,j}(\omega) v_{B,j}(\omega) \delta_{\omega',\omega}}{\sum_j \text{DOS}_{A,j}(\omega) v_{A,j}(\omega) \delta_{\omega',\omega} + \sum_j \text{DOS}_{B,j}(\omega) v_{B,j}(\omega) \delta_{\omega',\omega}} \quad (10)$$

where h is thermal interface conductance, \hbar is Planck's constant, $\text{DOS}_{A,j}$ is the phonon density of state for mode j , material A, v is the group velocity, f is the Bose-Einstein distribution function, α is the transmission coefficient, δ is Kronecker delta function. Regarding to electron-phonon coupling effect, two different temperatures assigned for electrons and phonons are coupled as Eq.(11),(12),

$$k_e \frac{\partial^2 T_e}{\partial z^2} - G_{ep}(T_e - T_p) = 0 \quad (11)$$

$$k_p \frac{\partial^2 T_p}{\partial z^2} + G_{ep}(T_e - T_p) = 0 \quad (12)$$

where G_{ep} is the electron-phonon coupling factor for metal materials [21, 30], k_e is the electron thermal conductivity and k_p is the lattice thermal conductivity of the metal, calculated with non-equilibrium molecular dynamics (NEMD). Since electrons are main carriers for heat transfer in most metals, $k_e \gg k_p$. With further mathematical deductions, interfacial resistance based on

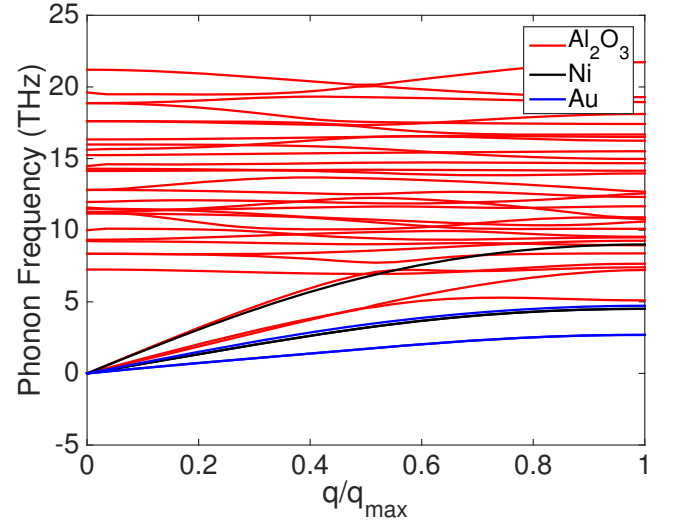


FIGURE 4: PHONON DISPERSION OF NI, AU, AND ALUMINUM OXIDE

two temperature model can be written as Eq.(13) [24],

$$R_i = R_{pp} + R_{ep} = \frac{1}{h_{pp}} + \left(\frac{k_e}{k_e + k_p}\right)^{3/2} \left(\frac{1}{G_{ep}k_p}\right)^{1/2} \approx \frac{1}{h_{pp}} + \left(\frac{1}{G_{ep}k_p}\right)^{1/2} \quad (13)$$

where R_i is the overall thermal interfacial resistance, R_{pp} , h_{pp} are lattice mismatch resistance and conductance respectively, and R_{ep} is interfacial resistance regarding to electron-phonon coupling effect.

In our study, theoretical estimations using TTM are shown in Tab.1 along with experiment results. Even through nickel has a much larger electron-phonon coupling factor, thus smaller electron-phonon resistance than gold, lattice mismatch still dominates interfacial resistance for both Ni and Au. Compared with gold, phonons in nickel share larger frequency domain with those in aluminum oxide, thus phonon-phonon resistance is lower than Au.

CONCLUSION

In this work, we measured thermal interfacial resistance between nickel and aluminum oxide, as well as that between gold and aluminum oxide. A 60% decrease of total resistance is observed when an intermediate metal layer nickel is inserted between gold and aluminum oxide layers. Theoretical calculation of two temperature model shows similar results with experiments, which also indicates the dominance of phonon mismatch between these two metals and aluminum oxide.

TABLE 1: COMPARISON BETWEEN TTM AND 3ω MEASUREMENT

R_i^*	DMM	e-p coupling	TTM	Experiment
Ni-Al ₂ O ₃	3.04	0.44	3.48	14.0
Au-Al ₂ O ₃	5.16	2.64	7.80	48.4

* $10^{-9}\text{m}^2\text{K/W}$

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